


## FACULTY PROFILE

<b>Name of Teaching Staff</b>	Mr.P S Havalagi	
<b>Designation</b>	Assistant Professor	
<b>Department</b>	Electronics and communication Engg	
<b>Date of Joining the Institution</b>	15.07.2013	
<b>Qualifications with Class / Grade</b>	M.Tech (VLSI & Embedded system)	
<b>Contact Details</b>	99801019405	
<b>Total Experience in Years</b>	12	
<b>Area of interest</b>	VLSI	
<b>Software skills</b>	Verilog HDL, VHDL, C	
<b>Papers Published</b>	01	
<b>Projects Guided</b>	12	
<b>STTP / FDP / Workshop/ Webinars Attended</b>	28	
<b>STTP / FDP / Workshop/ Webinars Organized</b>	3	
<b>Professional Membership</b>	IEEE	
<b>Subjects/Practical's Handled</b>	Verilog HDL,AEC,VLSI,CS.NA	